

August 1986 Revised March 2000

DM74LS11

Triple 3-Input AND Gate

General Description

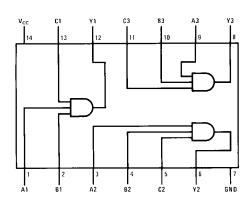
This device contains three independent gates each of which performs the logic AND function.

Ordering Code:

Order Number	Package Number	Package Description	
DM74LS11M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow	
DM74LS11N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide	

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Y = ABC

	Output		
Α	В	С	Y
Х	Х	L	L
Х	L	Χ	L
L	Х	Χ	L
Н	Н	Н	Н

- H = HIGH Logic Level L = LOW Logic Level X = Either LOW or HIGH Logic Level